

Software Release Notes, MK.4 ESD & Latch-Up Test System

Release 7.4 (Software, Middleware, and Firmware)

New Release: 7.4
Date: 2007-07-09
Previous Release: 7.3

Component	Version	Previous Version	Part Number
Software	1.14.4	1.14.4	70-901-205-00
Middleware	1.49.0.3	1.49.0.3	70-901-305-00
Firmware	1.02.20070709	1.00.20070215	70-901-305-00

Effectivity

This is a general release, applicable to all production and field systems.

Dependencies

The versions of the three components in this release (PC Software, Tester Middleware, and Tester Firmware) have been developed and tested to work with each other. Use of other version combinations is not recommended.

New features

Fixes and Improvements

Firmware

1. Added a V/I limit value program output adjustment to compensate for an offset in the power amplifier limit circuit.
2. Changed the V/I Calibration procedure to permit using either the 10 V or 100 V range, user selectable. Previously only the 10 V range was available, now the default is the 100 V range, due to hardware calibration issues.
3. Eliminated extraneous messages that were printed to the terminal window upon tester initialization; all useful information is now stored in the tester error_log file.

Known issues

1. Uploading large vector data sets can take considerable amount of time. The upcoming release will address this problem.
2. Preconditioning vectors are incorrect for the 1.2KHz vector clock speed. This frequency causes the stress pulse to disappear and the parking level to be ignored. It occurs only with this frequency and it occurs regardless of whether or not the

stress pin is actually being vectored. The problem will be fixed in the next release. For now, avoid using the 1.2KHz vector clock frequency.

Additional documentation

Figures